



SC10098C
PATENT

S.N. 09/709,893

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jefferson W. Hall et al
Date: July 15, 2004
Serial No.: 09/709,893
Filed: November 13, 2000
For: POWER CONVERSION INTEGRATED
CIRCUIT AND METHOD FOR
PROGRAMMING
Group: 2838
Examiner: Rajnikant B. Patel

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH
THE UNITED STATES POSTAL SERVICE
AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO :
Commissioner for Patents
P.O. Box 1450, Alexandria, Virginia 22313-1450,
ON: July 16, 2004
Date of Deposit

SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC
Name of Assignee

Lynia M. Chamara July 16, 2004
SIGNATURE DATE

AMENDMENT

HONORABLE COMMISSIONER OF PATENTS AND TRADEMARKS,
P.O. Box 1450
Alexandria, VA 22313-1450

SIR:

In response to the Office Action mailed April 19, 2004
please reconsider the above-identified patent application in
view of the amendments and remarks presented hereinafter.

Included herewith is a supplemental oath/declaration for
the re-issue application.

The original Letters Patent is also enclosed herewith for
surrender and replacement by the forthcoming re-issue patent.